



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

M. YAMAURA et al

Serial No. 10/021,173

Group Art Unit: 2823

Filed: December 19, 2001

Examiner: J. Maldonado

For: A SEMICONDUCTOR DEVICE AND A METHOD OF  
MANUFACTURING THE SAME

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1/22/03  
H-1820  
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TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner of Patents  
Washington, D.C. 20231

Sir:

In response to the Restriction Requirement dated  
December 13, 2002, Applicants respond as follows:

Applicants elect to prosecute the invention defined by  
the Examiner as Group I, including Claims 1-14, drawn to a  
semiconductor device. This election is made without traverse.

REMARKS

Examination is respectfully requested.

Respectfully submitted, \

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Date: January 13, 2003